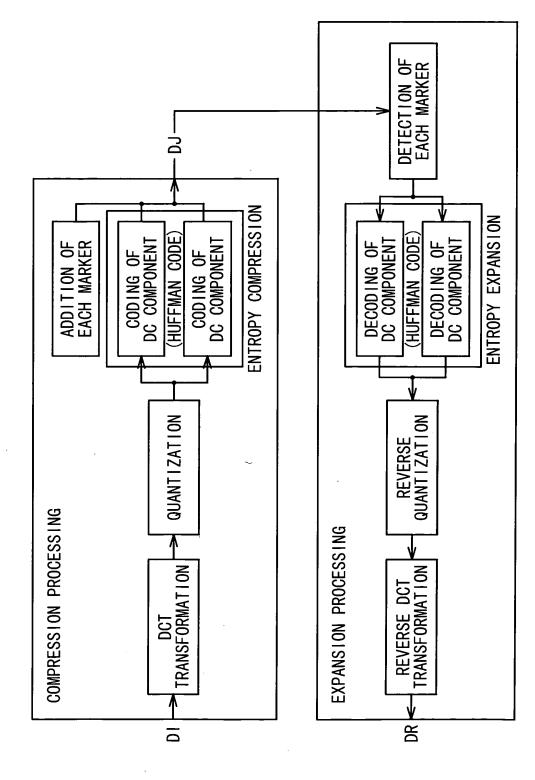
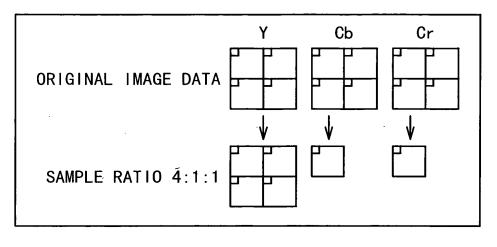
Fig. 1 PRIOR ART

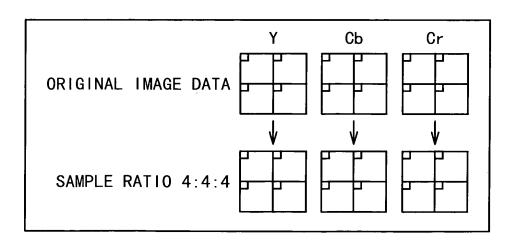


# Fig. 2A PRIOR ART



8 × 8 PIXELS=BLOCK

Fig. 2B PRIOR ART



8×8 PIXELS=BLOCK

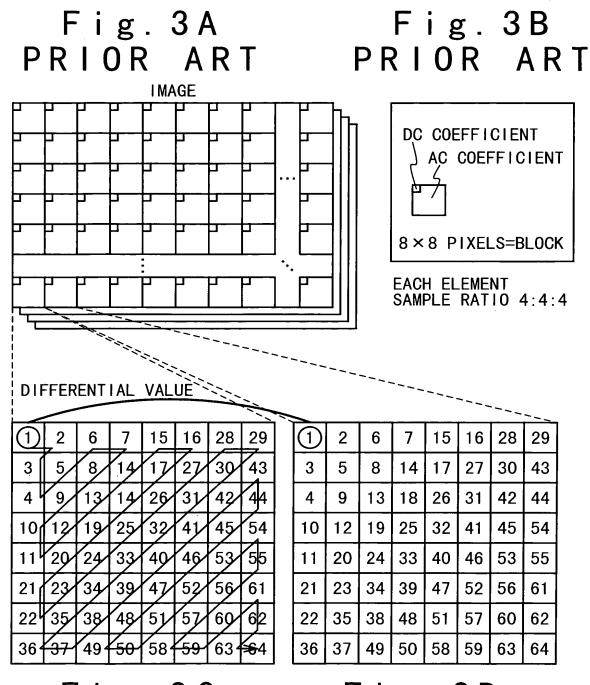
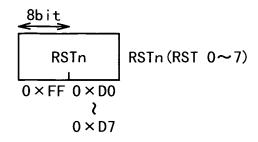
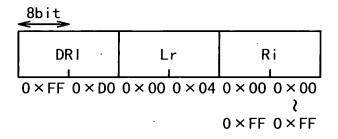


Fig. 3C Fig. 3D PRIOR ART

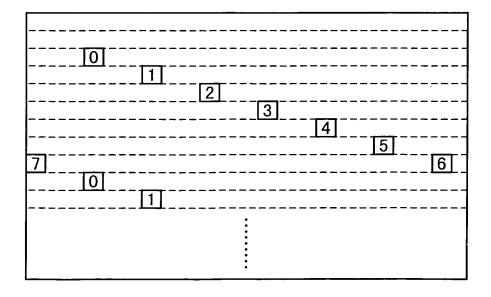
# Fig. 4A PRIOR ART



#### Fig. 4B PRIOR ART



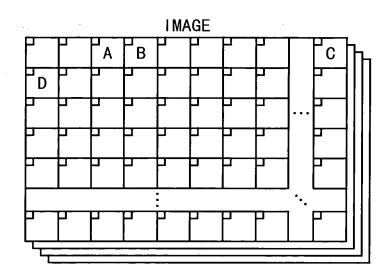
### Fig. 4C PRIOR ART



0~7 : RSTO~7

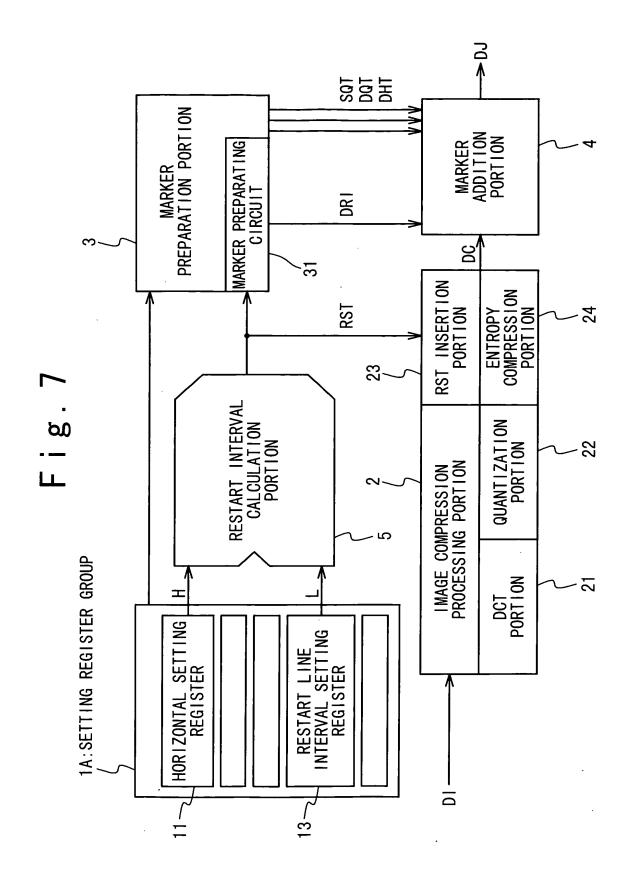
SQT DQT DHT MARKER PREPARATION PORTION MARKER ADDITION PORTION MARKER PREPARATING CIRCUIT DR. 3 PRIOR AR RST INSERTION PORTION ENTROPY COMPRESSION PORTION 23 QUANTIZATION PORTION 22 RST IMAGE COMPRESSION PROCESSING PORTION 2 Н ----SETTING REGISTER GROUP DCT PORTION HORIZONTAL SIZE SETTING REGISTER INTERVAL SETTING REGISTER  $\dot{\equiv}$ 12~

### Fig. 6 PRIOR ART



CORRELATION OF DC COEFFICIENT

A, B : HIGH CORRELATION C, D : LOW CORRELATION







 $Fig.\,\,8\,A$ 

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 $\boxed{0}\sim\boxed{7}$  : RST0 $\sim$ 7

Fig. 8B

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3	 	 
4	 	 ·
	 · :	
	 •	

0~7 : RST0~7

